The diagram shows a circuit with multiple J-K flip-flops labeled as $J$ and $K$, and their complemented counterparts labeled as $\bar{J}$ and $\bar{K}$. The circuit includes logic gates such as AND (&) gates and a summation (+) symbol. The inputs are labeled as Bit 0, Bit 1, Bit 2, and Bit 3, and they are connected to the flip-flops and the logic gates as per the diagram.